***Tentative Schedule of Work Items***

***(Up to Spring Break)***

1. Tuesday, March 10th – 3:30pm (~2.5 hours)
   1. Have SQA look over current completed work and check for traceability
   2. Begin data flow model for all components (UI and backend)
   3. SQA present coding standards to the team
2. Thursday, March 13th – 3:30pm (~2.5 hours)
   1. If data flow model not complete, finish
   2. Continue refinement of Memory and Registers design
   3. If Memory and Registers completed, begin refinement of Library and Definitions
3. Friday, March 14th – 6:00pm (~3 hours)
   1. If refinement of Library and Definitions not complete, finish
   2. Refine Error Detection design
   3. Refine Simulator design
   4. Refine Assembler design
4. *<Next meeting date TBD>*
   1. Begin pseudocode for Memory and Register operations
   2. Begin pseudocode for Library and Definitions
      1. If needed, push to next meeting
5. *<Next meeting date TBD>*
   1. Continue working pseudocode for Memory and Register operations
   2. Continue working on pseudocode for Library and Definitions
   3. Begin pseudocode for Error Detection
   4. Begin psuedocode for Assembler
   5. Begin psuedocode for Simulator
      1. If needed, push to next meeting
6. Thursday, March 20th – *Time TBD*
   1. GUI team present completed UI (w/o backend integration) with functionality to the team